FIG. 1

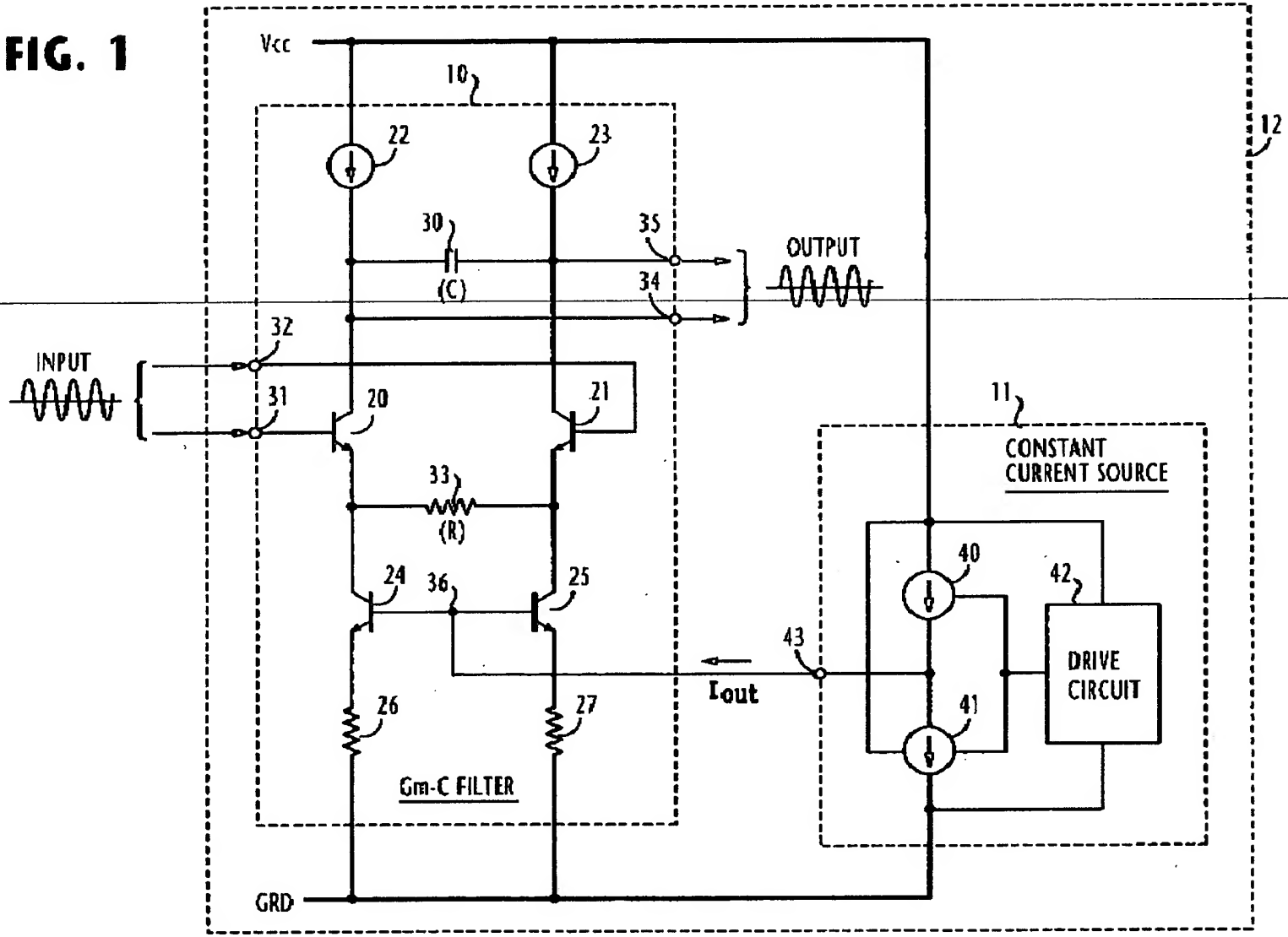

Vcc
10
12
22
23
30
35
OUTPUT
(C)
34
32
INPUT
31
20
21
11
CONSTANT
CURRENT SOURCE
33
(R)
40
42
24
36
25
43
DRIVE
CIRCUIT
Iout
26
27
41
Gm-C FILTER
GRD

FIG. 2

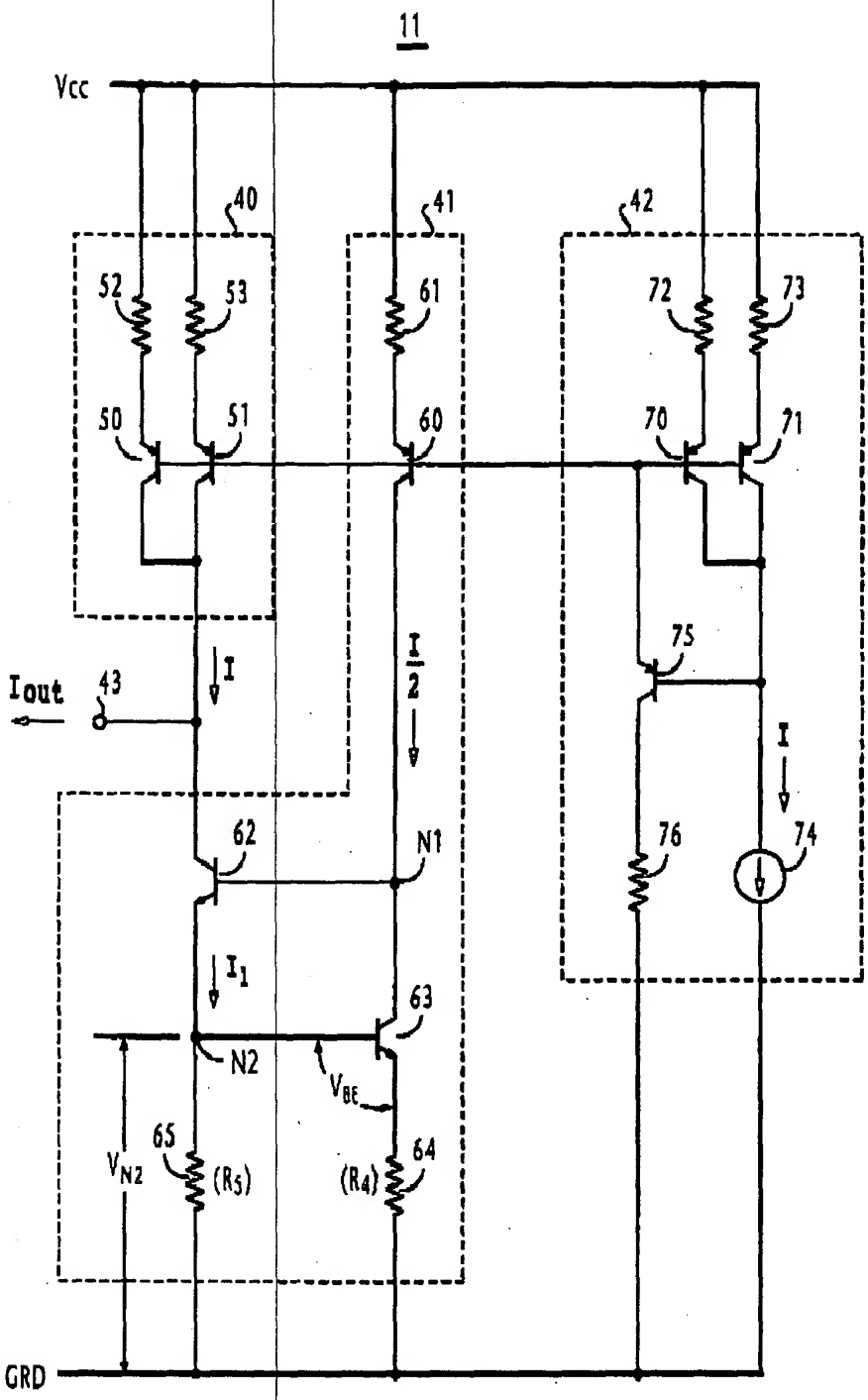

11
Vcc
40
41
42
52
53
61
72
73
50
51
60
70
71
Iout
43
I
I/2
75
I
62
N1
76
74
I1
63
N2
VBE
65
VN2
(R5)
(R4)
64
GRD

FIG. 3

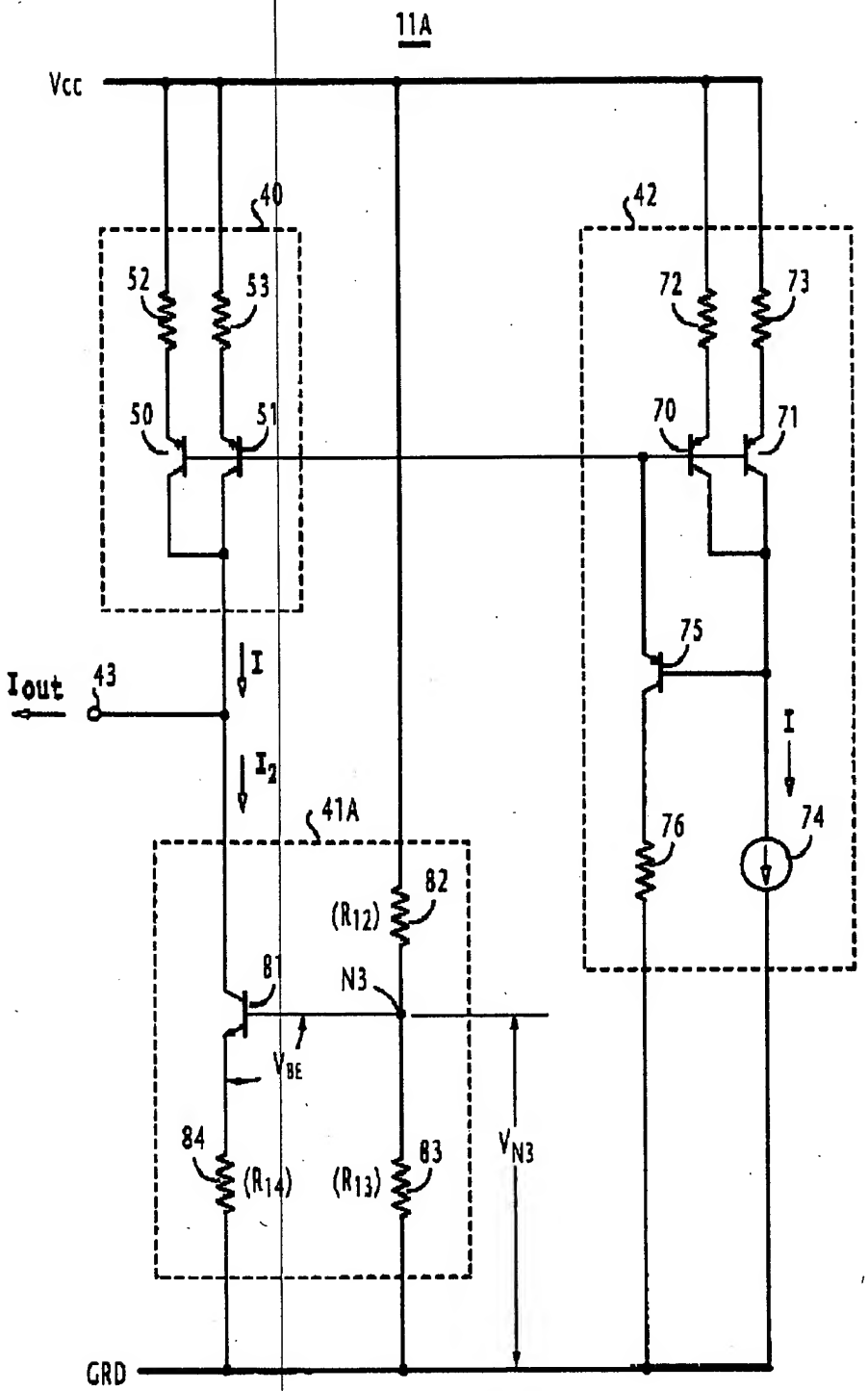
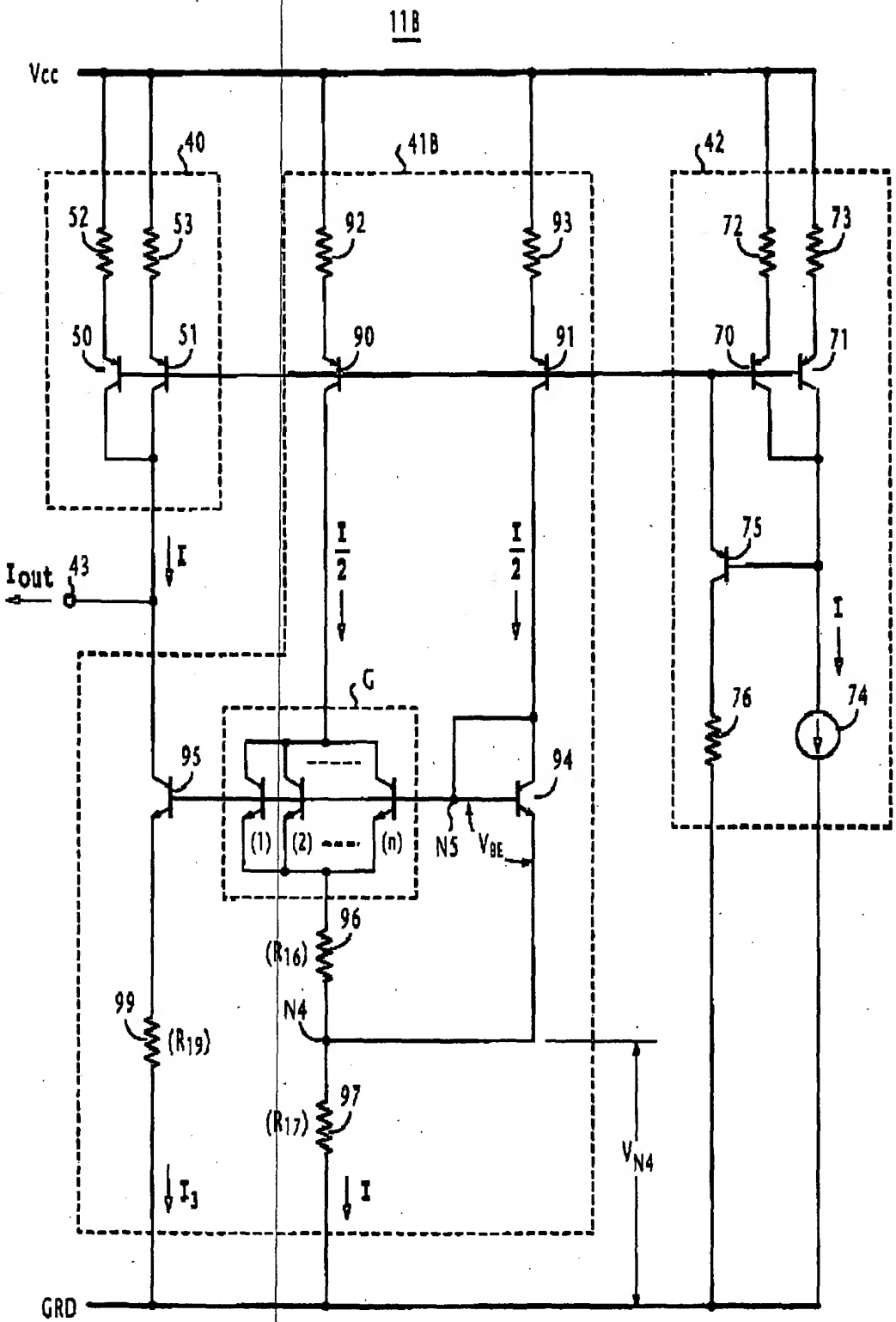

11A
Vcc
40
42
52
53
72
73
50
51
70
71
75
I
Iout
43
I
I2
41A
76
74
82
(R12)
B1
N3
VBE
84
(R14)
(R13)
83
VN3
GRD

FIG. 4


11B
Vcc
40
41B
42
52
53
92
93
72
73
50
51
90
91
70
71
Iout
43
I
I/2
I/2
75
I
G
76
74
95
94
(1)
(2)
(n)
N5
V_{BE}
96
(R_{16})
99
(R_{19})
N4
97
(R_{17})
V_{N4}
I_3
I
GRD